Vishwas Navada B
Hardware Design Engineer

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Full stack hardware engineer who can build a product from scratch to solve real-world problems. I love building stuff with electronics which requires hardware, programming and product designing skills. For further details about my work, please visit: https://vishwasnavada.github.io

WORK EXPERIENCE

R & D Engineer
Tejas Networks, ⌧

04/2019 – Present   *Bengaluru*

Achievements/Tasks
- Design and development of high-speed network switches
- Designing boards and development of FPGA based devices

Research Assistant
Robert Bosch Centre For Cyber-Physical Systems, IISc. ⌧

04/2018 – 03/2019   *Bengaluru*

Achievements/Tasks
- Built UWB beacon systems for Autonomous Navigation of Drones. ⌧
- Working on autonomous charging pads for drones.
- Designing Actuation and control of SMA based soft grippers.

Embedded Systems Design Engineer
Openwater.in, IISc, Bengaluru

03/2017 – 04/2018   *Bengaluru*

Achievements/Tasks
- Worked on the design and development of Electronic systems for water filtration unit
- Designed custom power supply to drive the whole system
- Designed and tested wireless water flow meters

EDUCATION

Bachelor of Engineering in Electronics and Instrumentation
Bangalore Institute of Technology (VTU), Bengaluru

08/2013 – 06/2017   *71.35*

Courses
- Analog Electronics, Digital Electronics, Process Instrumentation, Robotics and Control
- Final Year Project- Simultaneous localization and mapping using a Kinect in an indoor environment

SKILLS

Embedded C   FPGA   Python   RTOS

Circuit Design   3D Printing   Eagle CAD

PCB Designing   MatLab   LabVIEW

PERSONAL PROJECTS

IEncode Intelligent Encoder for Industrial Motors (08/2018 – 09/2018) ⌧
- Predictive maintenance of Industrial motors using Infineon's high precision 3D magnetic sensor TLE-493D

SewerSway (09/2018 – Present) ⌧
- Predictive maintenance of manholes and water systems using IoT and sensors

DriX (10/2016 – Present) ⌧
- Advanced Driver Assistance System

ACHIEVEMENTS

Hardware Hackathons (04/2015 – Present)
1st @ Reimagine Waste, IISc, 1st @ Infineon hackathon, 1st@ ABB Makeathon, 1st@ CeBIT IoT Hackathon, 3rd @ MediaTek Global IoT challenge, Best Geek Group @ Mercedes Benz Hack.Bangalore

Robo Racing (01/2016 – 05/2016)
1st @ Pravega, IISc, Aathmatrisha, PESU, NMIT fests.

Wrote an article about LoRa in Devopedia.org (06/2018 – 06/2018)
www.devopedia.org/lora

Quizzes and other competitions (05/2016 – 06/2017)
2nd @ BrainBox Quizzing, 2nd @TCS Tech Bytes, 1st@ Wright Brothers, Semifinalist International Ericsson Awards

INTERESTS

Analog Circuits   EDA   Product Design

Robotics   Mixed Signal Design

Embedded Systems   Drones   FPGA